

ABSTRACT

Frequency offset obtained by a frequency offset detecting circuit 2051 is output to a per-slot phase rotation
5 correcting circuit 207 and per-symbol phase rotation correcting
circuits 209 and 210. A maximum Doppler frequency (f_D) obtained
by an f_D detecting circuit 2052 is output to a weight factor
calculating circuit 208. The per-symbol phase rotation
correcting circuits 209 and 210 calculate a phase rotation
10 correction value $\Delta\theta_{\text{symbol}}$ of each symbol based on an amount
of phase rotation of a frequency offset and outputs the value
to multipliers 206 and 201. The per-slot phase rotation
correcting circuit 207 calculates a phase rotation correction
value $\Delta\theta_{\text{slot}}$ of each slot based on the amount of phase rotation
15 of the frequency offset and outputs the value to a weighted
adding circuit 204. In the weight factor calculating circuit
208, a weight factor (α) is calculated according to the detected
 f_D value and is output to the weighted adding circuit 204.